IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Tseng

Attorney Docket:

TSM03-0763

Filed:

Herewith

Examiner:

TBD

Serial No.:

TBD

Art Unit:

TBD

For:

A Compensated Linearity Voltage-Control-Capacitor Device by Standard CMOS Process

Mail Stop: Patent Application Commissioner for Patents P. O. Box 1450

Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

The Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed forms PTO/SB/08A & 08B that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed concurrently with the patent application.

Pursuant to 37 CFR 1.98(a)(2)(i), as amended, copies of U.S. Patents cited are not being submitted. However, Applicant has included copies of any non-patent literature.

21APRIL 2004

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Respectfully submitted,

Steven H. Slater

Attorney for Applicant

Reg. No. 35,361

Slater & Matsil, L.L.P. 17950 Preston Rd., Suite 1000 Dallas, TX 75252 (972) 732-1001 (phone) (972) 732-9218 (fax) U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE
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Sheet	1	of	2	Attorney Docket Number	TSM03-0763	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ^{2 (# known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		U.S. 6,407,412	06-18-2002	Iniewski et al.		
		U.S. 6,521,939	02-18-2003	Yeo et al.		
		U.S. 6,465,830	10-15-2002	Babcock et al.		
		U.S. 2003/0082461	05-01-2003	Carpi		
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (<i>if known</i>)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T⁰
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PTO/SB/08b (08-03)
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				Application Number	TBD	
INF	ORMATIO	N DISC	LOSURE	Filing Date	Herewith	
STATEMENT BY APPLICANT				First Named Inventor	Tseng	
				Art Unit	TBD	
(Use as many sheets as necessary)			essary)	Examiner Name	TBD	
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		NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SU, C.Y. et al., "Scalable RF MIS varactor model," <i>Electronics Letters</i> , Vol. 37 No. 12 (June 7, 2001) pp. 760-761.	

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